

542,388
Rec'd PCT/PTO 14 JUL 2005

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property
Organization
International Bureau



10/542388



(43) International Publication Date
29 July 2004 (29.07.2004)

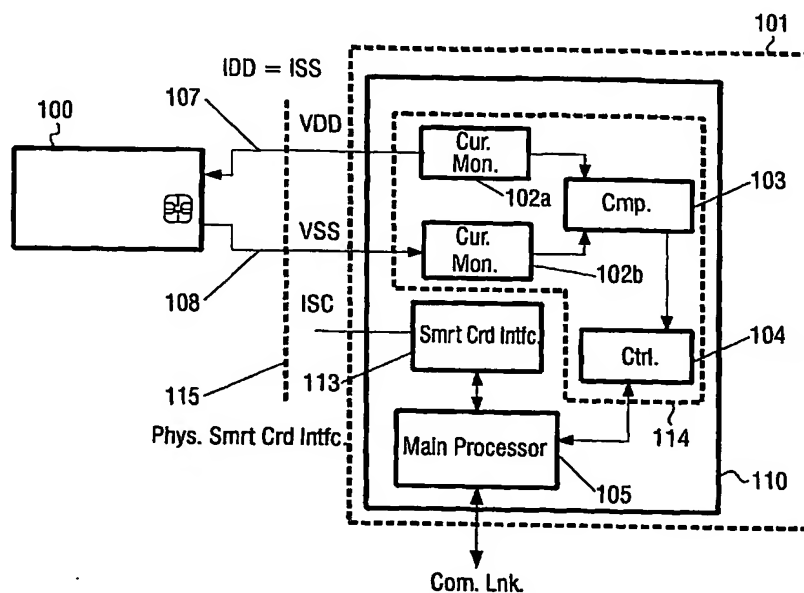
PCT

(10) International Publication Number
WO 2004/063970 A1

- (51) International Patent Classification: **G06K 7/00**
- (21) International Application Number:
PCT/IB2003/005971
- (22) International Filing Date: 4 December 2003 (04.12.2003)
- (25) Filing Language: English
- (26) Publication Language: English
- (30) Priority Data:
03100046.6 14 January 2003 (14.01.2003) EP
- (71) Applicant (for all designated States except US): **KONINKLIJKE PHILIPS ELECTRONICS N.V.** [NL/NL]; Groenewoudseweg 1, NL-5621 BA Eindhoven (NL).
- (72) Inventors; and
- (75) Inventors/Applicants (for US only): **BAKER, Keith** [GB/NL]; c/o Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL). **PLEUNIS, Johannes, M.** [NL/NL]; c/o Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL).
- (74) Agent: **GROENENDAAL, Antonius, W., M.**; Philips Intellectual Property & Standards, Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL).
- (81) Designated States (*national*): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.
- (84) Designated States (*regional*): ARIPO patent (BW, GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).
- Published:
— with international search report

[Continued on next page]

(54) Title: DETECTION OF TAMPERING OF A SMART CARD INTERFACE



(57) Abstract: This invention relates to a method (and a corresponding terminal) of detecting a presence of a circuit extending arrangement inserted between a physical interface, connected to a terminal, and a smart card, the physical interface being adapted to receive the smart card, the method comprising the steps of measuring at least one electrical characteristic of the physical interface, and determining whether a circuit extending arrangement, changing at least one characteristic of said physical interface, is coupled to said physical interface on the basis said measurement. In this way, simple and efficient detection of a tampering/spy circuit is provided.

WO 2004/063970 A1

W02004/063970 A1



For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

Detection of tampering of a smart card interface

FIELD OF THE INVENTION

The invention relates to a method, and a corresponding terminal, of detecting a presence of a circuit extending/tampering arrangement.

5 BACKGROUND OF THE INVENTION

Smart cards are commonly used in a wide range of applications for the purpose of authority check, payment, satellite TV, data storage, etc. As an example the health insurance and banking industries use smart cards extensively. A smart card usually resembles a credit card in size and shape, but typically contains an embedded microprocessor inside. A terminal with a card reader communicates with the microprocessor which controls access to the data on the card. Smart cards may e.g. be used with a smart card reader attached to or located in a personal computer to authenticate a user, etc. Smart card readers can also be found in mobile phones for SIM reading and vending machines.

For a private and/or in-home terminal with a smart card reading capability, such as a set top box (STB), integrated digital television (IDTV), Digital TVs, home gateways, access systems, GSMs, Internet audio sets, car systems, etc. the possibilities to spy on the electronic communication between a smart card and/or a secure access card and the terminal is much greater than in a public automated teller machine (ATM) or similar semi-public/public terminals e.g. used for/in connection with e-commerce. This enables attack on the smart card that is not possible with conventional smart card applications in public and/or semi-public terminals, due to the operation in a private sphere.

A financial and service industry consortium Finread in Europe is attempting to standardize a form of e-commerce terminals used in public ATM and personal computers (PCs), and also for future STBs, IDTV and similar home terminals. Up until now, the consortium has focused on expensive tamper detection and tamper resistant constructions for home e-commerce terminals. Embedded Finread is a part of the Finread consortium examining the issues of low-cost terminals for e-commerce such as IDTV, Jave terminals and STBs. The cost of temper resistant and other counter measures normally adopted for

dedicated terminals (i.e. ATMs) are considered to complex and/or expensive for the low cost home terminals.

A variety of smart cards/secure access cards exist, some with no sophisticated processing power, typically memory only cards. Other cards, typically so-called multi-
5 application/multi-function cards, comprise more advanced properties and functions typically providing secure authentication of the user/owner of the card e.g. in relation to gaining access to equipment, accounts, functions, transfer of money, e-commerce applications, etc.

Such cards have on-card dynamic data processing capabilities and allocate card memory into independent sections assigned to a specific function and/or application.

10 The multi-application/multi-function smart card is distributed by one issuer but allows two or more applications/functions to be resident on the smart card. Typically, advanced 32-bit processor cards are used for this purpose.

A Java smart card is a smart card with a Java Virtual Machine (JVM) that allows applications to enter and reside on the card. In this way, a Java smart card is a first
15 step towards multi-application smart cards.

As mentioned traditional smart cards only run one process, while a Java smart card have the capability to run multiple processes on the card, which is an enhancement of the smart card protection capability that allows the smart card not only to perform secured transaction, but also to monitor itself and the presence of an attack.

20 A device typically designated a sub-terminal is a device which includes some features of a terminal, i.e. user input, display, storage and a remote connection to the Internet, or a broadcast channel, but not all. Thus a terminal is complete, and examples are e.g. IDTV, STB or GSM (or similar cellular systems like 3G, UMTS, GPRS, etc.), but a sub-terminal is incomplete. In this way, a sub-terminal is a low-cost version of a terminal that provides some
25 but not all the functionality of a terminal. One example of a sub-terminal is e.g. a TV remote control. The sub-terminal may e.g. also be the conditional access module (typically denoted POD (point of deployment) in the US) of the conditional access system implemented in a STB and/or a TV. The conditional access module is a DVB based term derived from the Common Interface concept for DVB terminals.

30 Most system can be attacked successfully by a sufficiently resourced attacking entity. It is however necessary to provide sufficient defense (protection and/or detection) against a reasonably resourced security attack or at least provide counter measures that are sufficient to make a single form of attack no more successfully than others. It is also desirable to enable this in an inexpensive way.

A fake terminal can be used to gain access to an unwary user's pin-code or other application information of the card, which is hard to defend against, and would require other protection/detection schemes than provided by the present invention.

However, a typical security attack on genuine terminals/sub-terminals is the
5 insertion of circuit extending equipment like an extender, sampling circuit(s), emulation circuit(s), etc. in the electrical signal path(s) between a terminal and a smart card, where the circuit extending equipment is coupled both to the smart card to be observed and to any electrical equipment which is used to spy on the communication between the card and the terminal.

10 OBJECT AND SUMMARY OF THE INVENTION

It is an object of the invention to provide a method (and corresponding terminal) of detecting a presence of a circuit extending/tampering arrangement, where the method (and system) provides detection and/or protection against inserted fraud equipment.

15 A further object is to provide this in an inexpensive way. Yet a further object is to enable this using a minimum of electrical measurements.

This is achieved by a method of detecting a presence of a circuit extending arrangement inserted between a physical interface, connected to a terminal, and a smart card, the physical interface being adapted to receive the smart card), the method comprising the

20 steps of:

- measuring at least one electrical characteristic of the physical interface, and
- determining whether a circuit extending arrangement, changing at least one characteristic of said physical interface, is coupled to said physical interface on the basis said measurement.

25 In this way, detection of a circuit extending arrangement is provided.

In one embodiment, the step of measuring comprises:

- measuring a first current provided from said terminal to said smart card via said physical interface,
- measuring a second current returned from said smart card to said terminal,

30 and in that said method further comprises the step of

- comparing whether said first and said second current is substantially equal, and if not determining that a circuit extending arrangement is present.

Hereby, a very simple and reliant way of detecting a circuit extending arrangement is obtained.

In one embodiment, the method further comprises a step of:

- comparing said measured at least one electrical characteristic with at least one electrical characteristic as calibrated during manufacture.

5 In one embodiment, the physical interface has been calibrated to create at least one viable, but non-stable, electrical property at the physical level, the at least one property allowing normal transaction with said smart card, but causing the interface to fail if a circuit extending arrangement is coupled to said physical interface.

Hereby, the failure of the interface would thus resist the attack of tampering directly.

10 In one embodiment, the at least one non-stable electrical property relates to current and/or voltage characteristics of said physical interface.

In one embodiment, the method further comprises the step of:

- regulating the use of the smart card on the basis of said step of comparing.

15 In this way, the terminal could e.g. either warn the user and/or the card issuing authority (e.g. using the communications/IP link). The transaction, the access, etc. relating to the card would then be terminated and/or carefully monitored by the execution/issuing authority.

The present invention also relates to a terminal corresponding to the method according to the present invention.

20 More specifically, the invention relates to a terminal for detecting a presence of a circuit extending arrangement inserted between a physical interface, connected to said terminal, and a smart card, the physical interface being adapted to receive said smart card, the terminal comprising a monitoring circuit comprising

- means for measuring at least one electrical characteristic of the physical interface, and
- 25 • means for determining whether a circuit extending arrangement, changing at least one characteristic of said physical interface, is coupled to said physical interface on the basis an output of means for measuring.

In one embodiment, the means for measuring comprises:

- a first measure circuit measuring a first current provided from said terminal to said smart card via said physical interface,
- 30 • a second measure circuit measuring a second current returned from said smart card to said terminal,

and the terminal further comprises

- a comparator connected to said first and second measure circuit and adapted to compare whether said first and said second current is substantially equal, and if not generating a signal representing that a circuit extending arrangement is present.

5 In an alternative embodiment, the terminal further comprises a comparator for comparing said measured at least one electrical characteristic with at least one electrical characteristics as calibrated during manufacture.

10 In one embodiment, the physical interface has been calibrated to create at least one viable, but non-stable, electrical property at the physical level, the at least one property allowing normal transaction with said smart card, but causing the interface to fail if an circuit extending arrangement is coupled to said physical interface.

In one embodiment, the at least one non-stable electrical property relates to current and/or voltage characteristics of said physical interface.

In one embodiment, the terminal further comprises:

- 15
- means for regulating the use of the smart card on the basis of said signal from said comparator.

Further, the invention also relates to a computer readable medium having stored thereon instructions for causing one or more processing units to execute the method according to the present invention.

20 BRIEF DESCRIPTION OF THE DRAWINGS

Figure 1 schematically illustrates a smart card;

Figure 2 illustrates a smart card and a prior art terminal;

25 Figure 3 illustrates an example of an un-tampered circuit according to the present invention;

Figure 4 illustrates an example of a tampered circuit according to the present invention.

DESCRIPTION OF PREFERRED EMBODIMENTS

30 Figure 1 schematically illustrates a typical smart card. Shown is a security card/a smart card (100) that is well known in the prior art. Typically the card (100) has the form of standard size credit card, although the form, layout, size, etc. may vary. The card (100) typically comprises embedded memory, a processor/controller and input/output (I/O) used for communication with an appropriate card reader/(sub-)terminal (not shown) via a

number of contacts (100'). The shown contacts (100') (the size of which is exaggerated/enlarged for illustrative purposes) complies with the standard of ISO 7816 part 2 and comprises power supply (10), ground (11), three optional contacts/pins (12, 14, 15) that may be used for different functionality dependent on the specific card, a bi-directional input/output pin (13), check (16) and reset (17). All of these signals are provided by a terminal, receiving the card, to the smart card (100), and the terminal is expected to monitor the bi-directional input/output (13) according to the standard protocols in order to observe the response of the smart card (100).

Such a card (100) may be used to store information like PIN-codes, identification information, personal information, security information, etc.

Figure 2 illustrates a smart card and a typical prior art terminal. Shown are the smart card (100) and the terminal (101) that communicates via a physical smart card interface (115). The terminal (101) comprises a main processor (105) and a generalized standard smart card interface (106) preferably integrated into or embedded in an integrated circuit (IC) (110) in the terminal (101). Optionally the terminal (101) also comprises a communications/IP link e.g. useful for various e-commerce applications and/or other functions.

When inserted in a private and/or home terminal/sub-terminal (101) (both forth denoted terminal), a smart card (100) would be supplied with power from a power supply from a central source, and the ground would be the central ground of the terminal's (101) electrical systems, since a smart card (100) does not have a power supply. Control signals of the physical smart card interface (115) in the terminal (101) would typically be provided by a serial smart card interface (106) programmed by the central processor(s) (105) of the terminal (101) to execute the standard protocols to address the smart card's physical interface (115). This is a very cheap and flexible arrangement, which allows developers of terminal/STB software to use standard electronic interfaces and processes to access the cards. However, the usage of a standard serial interfaces (106) makes the terminal (101) open to forms of tampering by spying on the communication, as described above.

Figure 3 illustrates an example of an un-tampered circuit according to the present invention. Shown are a smart card/secure access card (100) and a terminal (101) modified according to the present invention. The smart card (100) and the physical interface (115) correspond to the ones shown and explained in connection with Figure 2.

The terminal (101) corresponds to the one shown and explained in connection Figure 2 with the exceptions that it comprises a specialized smart card interface/controller (113) instead of the generalized interface (106 in Figure 2) and that it further comprises a

monitoring/detection circuit (114), preferably integrated in/embedded into an IC (110), constituting the functionality of the terminal (101), and connected to the main processor (105) and the physical smart card interface (115).

Interface pins or other connections of the IC (110) is then directly coupled to the mechanical interface (115) that couples to the smart card (100). In this way, the IC (110) may be equipped with additional functionality allowing for electrical measurements of the physical/mechanical interface (115) to be made in order to detect tampering with the interface (115) which could allow for spying on the communication via the interface (115).

In the shown embodiment, the terminal (101) comprises a monitoring process done by the monitoring/detection circuit (114) that monitors and compares certain electrical characteristics of the physical interface (115), as explained in greater detail in the following.

As mentioned, a smart card (100) is an electrical circuit without internal power source(s) where a terminal (101) supplies the energy, i.e. the currents in the smart card (100). This means that the sum of all DC and AC currents supplied to the card (I_{SC}) must be returned to the source, i.e. the smart card interface/controller (113) in the IC (110) in the terminal (101). If there is a leakage of current (either DC and/or AC) from the source that is not returned back to the source then either interference and/or tampering must be present. Such tampering may e.g. be a monitoring/spy circuit, an extender, etc. with powered sensors/amplifiers. The monitoring/detection circuit (114) according to the present invention is able to sense either the AC or DC loss of current to return paths, i.e. sources, other than the terminal itself.

In the embodiment shown in Figure 2, the monitoring/detection circuit (114) more specifically comprises a first current monitor (102a) coupled to a VDD connection (e.g. the power pin (10) in Figure 1) and measuring/monitoring the current (denoted I_{DD}) and a second current monitor (102b) coupled to a VSS connection (e.g. the ground pin (11) in Figure 1) and measuring/monitoring the current (denoted I_{SS}). The first and second current monitors (102a, 102b) are both connected to a comparator circuit (103) that compares I_{DD} and I_{SS} in order to determine if they are (substantially) equal or different (at all or by a factor greater than a predetermined factor), i.e. if I_{SS} (substantially) = I_{DD} or not. If they are equal, it signifies that the current introduced to the smart card (100) from the terminal (100) is also returned again signifying that no tampering circuit has been inserted. If the currents are different (e.g. by more than a margin taking into account normal interference), it signifies that a spy circuit, tampering circuit, extending arrangement, etc. has been inserted.

The comparator (103) is connected to a controller (104) that on the basis of the signal received from the comparator (103) generates a control signal that is supplied to the main processor(s) (105). In this way, the main processor(s) (105) may initiate appropriate action(s) if a tampering circuit is detected. In the shown example, no tampering circuit is inserted and therefore the current introduced (I_{SS}) into the card is (substantially) equal to the current (I_{DD}) returned back to the terminal (100).

The detection circuit (110) may e.g. be a standard current mirror circuit comparing I_{SS} and I_{DD} using a window function determined by the controller (104) and executed by the comparator (103).

Preferably, the capability to re-calibrate the interface between the card (100) and the terminal (101) is not available in the terminal (101). At least not without, the use of special equipment only available at the manufacturing site.

An alternative embodiment comprises calibration of the interface used to create viable, but non-stable electrical properties at the physical level, e.g. time/timing, voltage and/or current, of the interface to the smart card (100). These properties should be viable enough to allow normal transaction with the card, but so dedicated to the electrical conditions that an insertion of circuit extending arrangement (111, 112), e.g. an extender, monitoring device, etc., would cause the interface to fail. The failure of the interface would thus resist the attack of tampering directly.

One implementation of this is to use an impedance-based method that uses current and/or voltage characteristics of the interface to create electrical conditions that are fragile. This is e.g. possible by creating driver circuits in the terminal (101) that are programmable to the electrical impedance of the signal path. Specifically these could use the signal path reflection characteristics. One condition for this may e.g. be that the signal transition time of the driver and a significant proportion of the flight time from source to receiver. In this case, the signal path has the properties of a transmission line thereby making an impedance-based method very practical.

Yet a further embodiment comprises a monitoring process that compares known electrical characteristics of the interface (as calibrated during manufacture) and the present conditions. The monitoring process would regulate the use of the smart card. If the present condition(s) of the actual card inserted into the terminal deviated from the calibrated conditions (e.g. at all and/or within a predetermined margin) then the terminal could e.g. either warn the user and/or the card issuing authority (e.g. using the communications/IP link).

The transaction, the access, etc. relating to the card would then be terminated and/or carefully monitored by the execution/issuing authority.

Figure 4 illustrates an example of a tampered circuit according to the present invention. Shown is the arrangement shown in Figure 3 but with an extender (111) inserted and coupled to a spy/monitoring circuit (112). As the inserted extender (111) and/or spy
5 circuit (112) introduces a 'leakage' of current, then I_{SS} will be different from I_{DD} , i.e. all the current supplied by the terminal is not received back, which will be detected by the comparator (103) and signaled by the controller (104) to the main processor (105). In this way, attempts at spying, tampering, etc. is readily detected by simple means.

CLAIMS:

1. A method of detecting a presence of a circuit extending arrangement (111, 112) inserted between a physical interface (115), connected to a terminal (101), and a smart card (100), the physical interface (115) being adapted to receive the smart card (100), the method comprising the steps of:

- 5
- measuring at least one electrical characteristic of the physical interface (115), and
 - determining whether a circuit extending arrangement (111, 112), changing at least one characteristic of said physical interface (115), is coupled to said physical interface (115) on the basis said measurement.

10 2. Method according to claim 1, characterized in that said step of measuring comprises:

- measuring a first current provided from said terminal (101) to said smart card (100) via said physical interface (115),
 - measuring a second current returned from said smart card (100) to said terminal (101),
- 15

and in that said method further comprises the step of

- comparing whether said first and said second current is substantially equal, and if not determining that a circuit extending arrangement (111, 112) is present.

20 3. A method according to claim 1, characterized in that said method further comprises a step of:

- comparing said measured at least one electrical characteristic with at least one electrical characteristic as calibrated during manufacture.

25 4. A method according to claim 1, characterized in that said physical interface (115) has been calibrated to create at least one viable, but non-stable, electrical property at the physical level, the at least one property allowing normal transaction with said smart card (100), but causing the interface to fail if an circuit extending arrangement (111, 112) is coupled to said physical interface (115).

5. A method according to claim 4, characterized in that said at least one non-stable electrical property relates to current and/or voltage characteristics of said physical interface (115).

5

6. Method according to claims 1 – 5, characterized in that the method further comprises the step of:

- regulating the use of the smart card (100) on the basis of said step of comparing.

10

7. A terminal for detecting a presence of a circuit extending arrangement (111, 112) inserted between a physical interface (115), connected to said terminal (101), and a smart card (100), the physical interface (115) being adapted to receive said smart card (100), the terminal (100) comprising a monitoring circuit (114) comprising

15

- means (102a, 102b) for measuring at least one electrical characteristic of the physical interface (115), and
- means (104) for determining whether a circuit extending arrangement (111, 112), changing at least one characteristic of said physical interface (115), is coupled to said physical interface (115) on the basis an output of means for measuring (102a, 102b).

20

8. A terminal according to claim 7, characterized in that said means for measuring comprises:

- a first measure circuit (102a) measuring a first current provided from said terminal (101) to said smart card (100) via said physical interface (115),
- a second measure circuit (102b) measuring a second current returned from said smart card (100) to said terminal (101),

25

and in that said terminal further comprises

- a comparator (103) connected to said first and second measure circuit (102a, 102b) and adapted to compare whether said first and said second current is substantially equal, and if not generating a signal representing that a circuit extending arrangement (111, 112) is present.

30

9. A terminal according to claim 7, characterized in that said terminal further comprises a comparator (103) for comparing said measured at least one electrical characteristic with at least one electrical characteristics as calibrated during manufacture.

10. A terminal according to claim 7, characterized in that said physical interface (115) has been calibrated to create at least one viable, but non-stable, electrical property at the physical level, the at least one property allowing normal transaction with said smart card (100), but causing the interface to fail if an circuit extending arrangement (111, 112) is coupled to said physical interface (115).
11. A terminal according to claim 10, characterized in that said at least one non-stable electrical property relates to current and/or voltage characteristics of said physical interface (115).
12. A terminal according to claims 7 – 11, characterized in that the terminal (101) further comprises:
- means (106) for regulating the use of the smart card (100) on the basis of said signal from said comparator.
13. A computer readable medium having stored thereon instructions for causing one or more processing units to execute the method according to any one of claims 1 – 6.

1/2

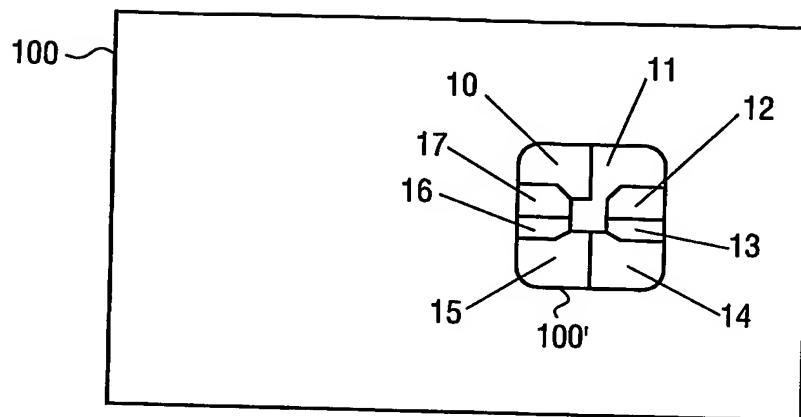


FIG. 1

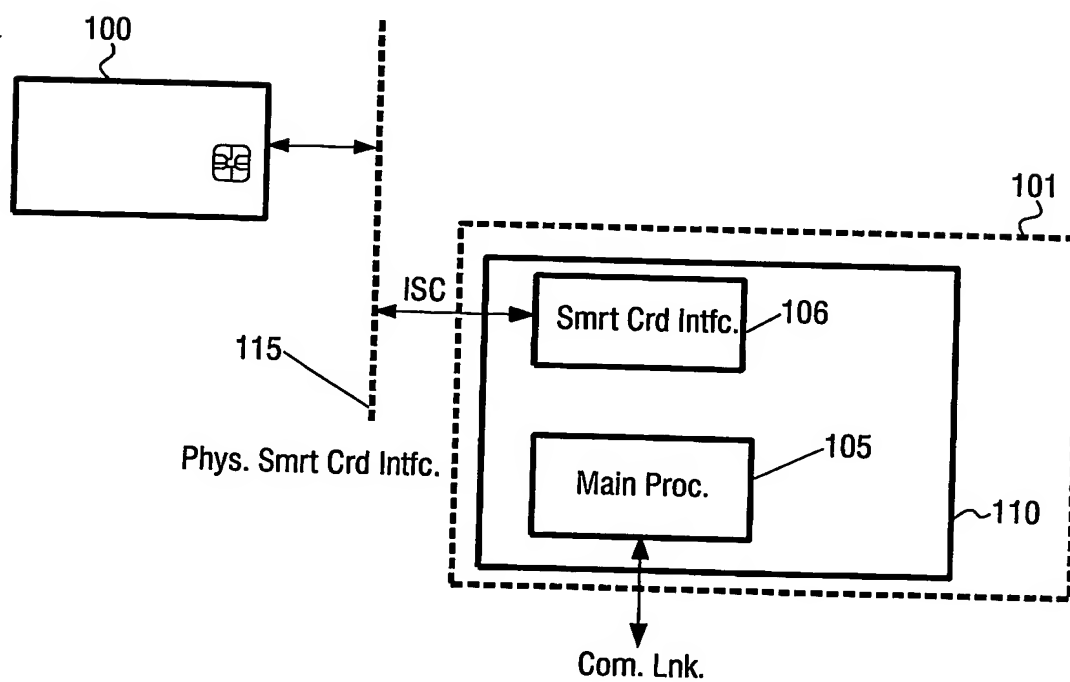


FIG. 2

2/2

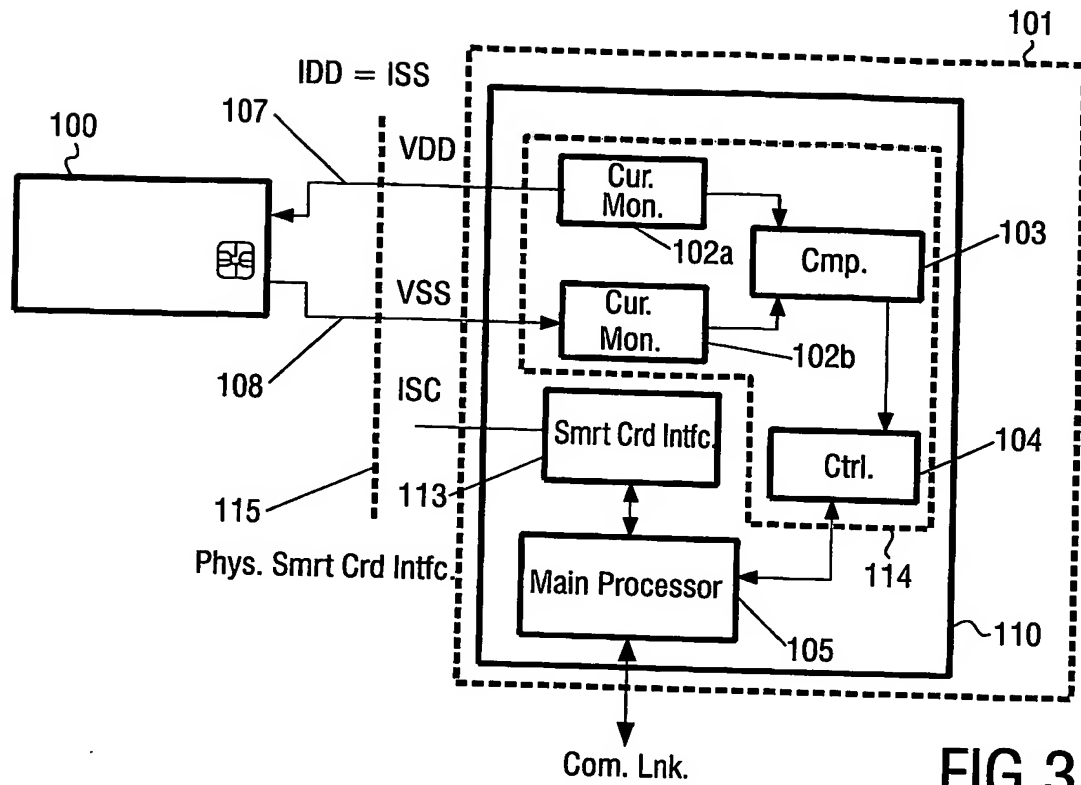


FIG. 3

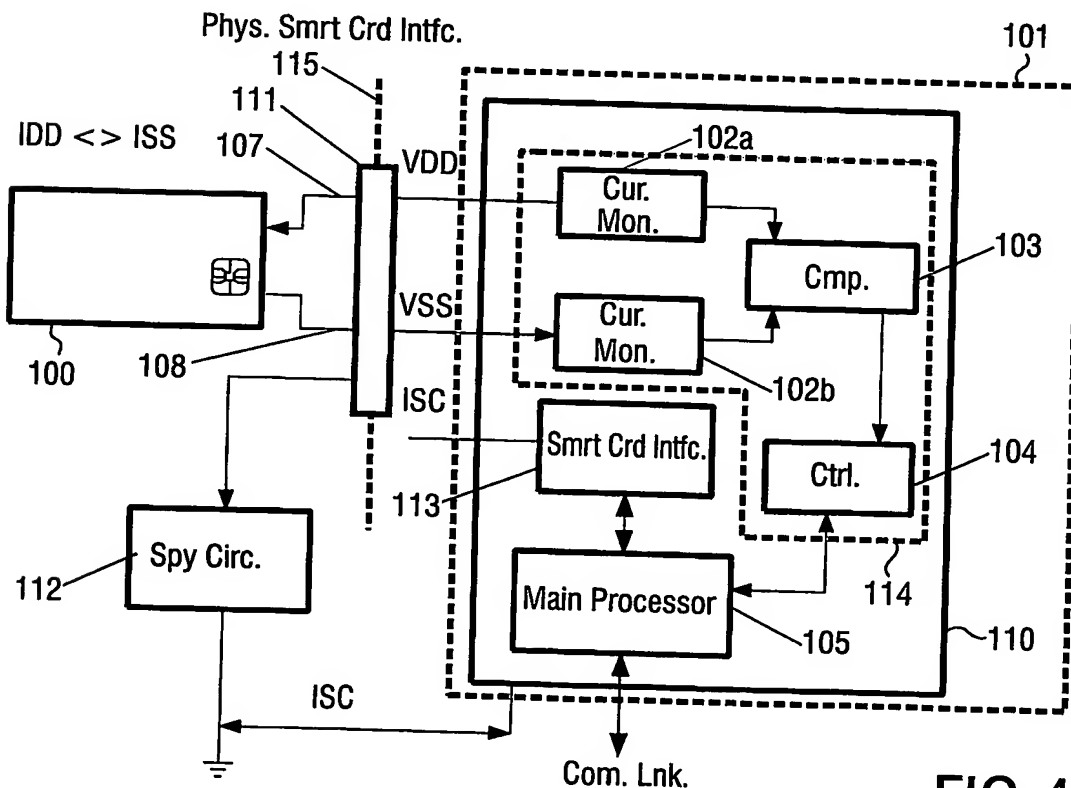


FIG. 4

INTERNATIONAL SEARCH REPORT

Internat. Application No

P 03/05971

A. CLASSIFICATION OF SUBJECT MATTER
IPC 7 G06K7/00

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
IPC 7 G06K

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, PAJ

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	FR 2 659 770 A (SEXTANT AVIONIQUE) 20 September 1991 (1991-09-20) the whole document	1,3,6,7, 9,12,13
X	EP 0 619 560 A (PLESSEY TELECOMM) 12 October 1994 (1994-10-12) the whole document	1,3,6,7, 9,12,13

☐ Further documents are listed in the continuation of box C.☒ Patent family members are listed in annex.

* Special categories of cited documents:

- *A* document defining the general state of the art which is not considered to be of particular relevance
- *E* earlier document but published on or after the international filing date
- *L* document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)
- *O* document referring to an oral disclosure, use, exhibition or other means
- *P* document published prior to the international filing date but later than the priority date claimed

- *T* later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
- *X* document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
- *Y* document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
- *G* document member of the same patent family

Date of the actual completion of the international search

19 March 2004

Date of mailing of the international search report

31/03/2004

Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2
NL - 2280 HV Rijswijk
Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,
Fax (+31-70) 340-3016

Authorized officer

Schmidt, R

INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No
PCT/JP 03/05971

Patent document
cited in search report

Publication
date

Patent family
member(s)

Publication
date

FR 2659770	A	20-09-1991	FR	2659770 A1	20-09-1991
EP 0619560	A	12-10-1994	EP	0619560 A1	12-10-1994
			US	5416423 A	16-05-1995
			ZA	9401505 A	07-07-1995